

TFT-DISPLAYS / Q-SERIES



PIN	All function
1	GND
2	VDD (3V)
3	BL_E
4	RS
5	WR
6	RD
7 ~ 22	DB0 ~ DB15
23	NC / TE
24	NC/ For CTP
25	CS
26	RESET
27	NC(UD)
28	NC(LR)
29 ~ 32	For TP function CTP & RTP
33-34	B/L power (Gnd)
35-36	B/L power (VCC)

The Q-series of **RAYSTAR Optonics** has a uniform 36-pin standard MCU interface. All modules have a back board with integrated controller SSD1963, which can be driven by 8 or 16 bit. Due to the unified interface it is not necessary to change the board layout if there is a display resizing within the Q-series.

ADVANTAGES OF Q-SERIES

- » Unified standard MCU interface
- » Implemented display controller SSD1963
- » Optional 8 bit / 16 bit control
- » Integrated LED backlight driver
- » Optional with resistive or capacitive touch panel

SIZE	MODEL NUMBER	DISPLAY RESOLUTION	BRIGHTNESS [cd/m ²]	TYP. BACKLIGHT LIFE TIME [h]	INTERFACE 16 BIT MCU	OPERATING TEMPERATURE [°C]	RESISTIVE TOUCH PANEL	CAPACITIVE TOUCH PANEL (PCAP)
Q-SERIES								
3.5"	RFC350Q-EIW-DBN	320×240	420	50 k	•	-20...+70		
3.5"	RFC350Q-EIW-DBS	320×240	300	50 k	•	-20...+70	•	
3.5"	RFC350Q-EIW-DBC	320×240	340	50 k	•	-20...+70		•
4.3"	RFC430Q-EIW-DBN	480×272	500	50 k	•	-20...+70		
4.3"	RFC430Q-EIW-DBS	480×272	300	50 k	•	-20...+70	•	
4.3"	RFC430Q-EIW-DBC	480×272	340	50 k	•	-20...+70		•
5.0"	RFC50YQ-1IW-DBN	800×480	450	20 k	•	-20...+70		
5.0"	RFC50YQ-1IW-DBS	800×480	300	20 k	•	-20...+70	•	
5.7"	RFC570Q-EIW-DBN	320×240	500	50 k	•	-20...+70		
5.7"	RFC570Q-EIW-DBS	320×240	350	50 k	•	-20...+70	•	
5.7"	RFC570Q-EIW-DBG	320×240	400	50 k	•	-20...+70		•
7.0"	RFC700Q-1IW-DBN	800×480	450	50 k	•	-20...+70		
7.0"	RFC700Q-1IW-DBS	800×480	300	50 k	•	-20...+70	•	
7.0"	RFC700Q-1IW-DBG	800×480	380	50 k	•	-20...+70		•
8.0"	RFC800Q-1IW-DBN	800×480	450	20 k	•	-20...+70		
8.0"	RFC800Q-1IW-DBS	800×480	320	20 k	•	-20...+70	•	
8.0"	RFC800Q-1IW-DBC	800×480	360	20 k	•	-20...+70		•
10.2"	RFC1020Q-1IW-DBN	800×480	350	20 k	•	-20...+70		
10.2"	RFC1020Q-1IW-DBS	800×480	250	20 k	•	-20...+70	•	
10.2"	RFC1020Q-1IW-DBG	800×480	280	20 k	•	-20...+70		•

Reserve technical changes!